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INSTRUCTIONS (1) Question 1 is compulsory. (2) Attempt any three from the remaining questions.
(3) Draw neat diagrams wherever necessary.

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Digital Logic Design

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EE 110 Practice Problems - Digital Logic - Fall 2008

3. Build a register file out of (already built) registers, multiplexers, decoders, and logic gates that has the following inputs: • read select line • two write select lines, selecting two registers that can be written to. • two write data lines, for writing data to the two registers selected.

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